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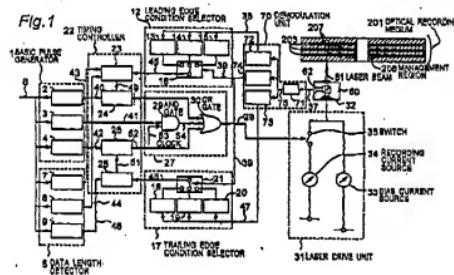
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**(54) OPTICAL RECORD MEDIUM, METHOD FOR RECORDING ON OPTICAL RECORD MEDIUM, AND APPARATUS FOR RECORDING ON OPTICAL RECORD MEDIUM**

(57) An optical recording medium of the present invention includes a substrate (202), a protection plate (205), and a plurality of information layers (203) sandwiched between the substrate and the protection plate. An information signal is recorded as a length of a mark and a length of a space between two marks. The mark is formed when the information layer is irradiated by light received through the substrate. The information layers

have a management region (208) recording a leading edge recording condition and a trailing edge recording condition. The leading edge recording condition is used to change a recording start position for forming a leading edge of the mark. The trailing edge recording condition is used to change a recording end position for forming a trailing edge of the mark. Each of the leading and trailing edge recording condition is depending on which information layer is recorded.



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**Description****BACKGROUND OF THE INVENTION****5 1. TECHNICAL FIELD**

[0001] The present invention relates to an optical recording medium and a recording and reproducing apparatus capable of recording or reproducing signals of high density by irradiating a plurality of thin recording films formed on a substrate with a high energy beam such as laser beam.

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**2. BACKGROUND ART**

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[0002] Recently, commercialization of optical recording media capable of recording, erasing and reproducing information, and research and development of rewritable optical recording medium of high density capable of recording moving images of high quality are actively done. Known examples of rewritable optical recording media include the phase change optical recording medium having a disk-shaped substrate and an overlaying information layer such as a thin chalcogenide film using Te or Se as the base such as Ge-Sb-Te or In-Se, or a semimetallic thin film such as In-Sb. A magneto-optical recording medium having a thin metal film of Fe-Tb-Co or the like as an information layer is also known. Further, a write-once optical recording medium using a dye material is also known.

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[0003] In the phase change optical recording medium, for example, an information layer composed of the phase change material is momentarily irradiated with a laser beam focused in a light spot of a sub-micron order size, and the irradiated area is locally heated up to a specified temperature. When heated over a crystallization temperature, the irradiated area is transitioned from amorphous state to crystalline state, and when further heated over a melting point to be fused and then quenched, it is transitioned to amorphous state. Either the amorphous state or the crystalline state is defined as recorded state or erased state (unrecorded state), and by forming in a pattern corresponding to the information signal, reversible information recording or erasing may be realized. The optical characteristic of crystalline state is different from that of the optical characteristic of amorphous state, and by making use of this difference, the signal can be reproduced by detecting optically as a reflectivity change or a transmissivity change.

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[0004] In a magneto-optical recording medium, for example, a magneto-optical recording thin film is irradiated with a focused laser beam, and is locally heated to a specified temperature. By applying a magnetic field simultaneously when heating, the direction of magnetization of the magneto-optical recording thin film is reversed depending on the information, so that the information can be recorded or erased.

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[0005] As a high density data recording method on an optical recording medium, mark length recording is known. In mark length recording, various mark lengths are recorded between various space lengths, and recorded information is assigned in both mark length and space length. In other proposed optical recording medium, in order to increase the capacity of the optical recording medium outstandingly, a plurality of information layers are provided, and a laser beam is irradiated from one side, and information is recorded or rewritten in each information layer.

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[0006] When recorded in a phase change recording medium by the mark length recording method, the amorphous region is defined as a mark, and the crystalline region as a space. In order to record at higher density in this recording medium, the length of the marks and spaces to be recorded must be shortened. However, when the space length is shorter, the heat at the trailing edge of the recorded mark has an effect on temperature rise at the leading edge of the mark to be recorded next, or the heat at the leading edge of the mark recorded next has an effect on the cooling process of the preceding mark, that is, so-called heat interference occurs. By this heat interference, the edge position of the leading edge or trailing edge of the recording mark is deviated from the proper position, and the bit error rate is deteriorated on reproduction.

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[0007] To solve this problem, for example, it has been proposed to prevent the mark edge position from varying due to heat interference due to recording by varying the leading edge position and trailing edge position of recorded pulse so that the space length space recording may be a specified length, when the space is shorter than the specified length on detecting the length of space between marks (Japanese Patent No. 2,679,598, corresponding United States Patent No. 5,490,128).

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[0008] However, when a plurality of information layers are provided, each information layer must be irradiated with a proper quantity of laser beam. For example, as shown in Fig. 11, in an optical recording medium having two information layers, that is, a first information layer 904 and a second information layer 908, it must be designed so that a laser beam 901 may be irradiated to the second information layer which is at the remotest side from the irradiation direction of the laser beam 901, and may be returned to the incident side to reproduce the information. Accordingly, the transmissivity of the laser beam 901 passing through a protection layer 903, the first information layer 904 and a protection layer 905 at the side closer to the incident side of the laser beam 901 is generally designed to be higher than about 50%. To obtain such transmissivity higher than 50%, the protection layer 903 and protection layer 905 which are thin

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film adjacent to the first information layer 904 are made of nonmetallic transparent layers. In order that the second information layer 908 positioned at the remote side from the incident side of the laser beam 801 may have a greater quantity of reflected light, generally, a reflection layer 910 made of a metal material is disposed adjacently to the second information layer 908.

- 5 [0009] In such configuration, depending on whether there is a thin metal film adjacently to the information layer or not, a difference is made in the heating and cooling condition of the information layer. That is, if each layer is irradiated with a laser beam of same pulse condition, different recording marks are provided in layers, and if the recording mark edge is moved (edge shift) and information is reproduced between layers, jitter of reproduced signal increases in either information layer, thereby increasing the error rate.

## 10 SUMMARY OF THE INVENTION

[0010] It is hence an object of the present invention, relating to an optical recording medium having a plurality of information layers, to present an optical recording medium and an optical recording and reproducing apparatus capable of forming recording marks of a small edge shift in any one of the information layers.

[0011] In accordance with one aspect of the present invention, there is an optical recording medium includes a substrate, a protection plate, and a plurality of information layers sandwiched between the substrate and the protection plate. The plurality of information layers are for recording information signals as a length of mark and a length of a space between two marks. The mark is formed when irradiated by light received through the substrate. The plurality of information layers have a management region recording a leading edge recording condition and a trailing edge recording condition. The leading edge recording condition is used to change a start position for forming a leading edge of the mark, the leading edge recording condition is depending on which information layer is recorded. The trailing edge recording condition is used to change a recording end position for forming a trailing edge of the mark, the trailing edge recording condition is depending on which information layer is recorded.

20 [0012] Preferably, the leading edge recording condition may include a condition depending on the length of the mark and the length of the space preceding the mark, and the trailing edge recording condition may include a condition depending on the length of the mark and the length of the space succeeding the mark.

[0013] Preferably, the leading edge recording condition and the trailing edge recording condition may include a condition of whether data have been recorded or not in one of the information layers positioned at light incident side.

30 [0014] Preferably, the management region may be provided in one of the plurality of information layers.

[0015] Preferably, the management region may be provided in one of the information layers at light incident side.

[0016] Preferably, each of the plurality of information layers has a identifier for information layer.

[0017] Preferably, each of the plurality of information layers may have a recording identifier on whether data have been recorded or not therein. These recording identifier may be recorded in the management region.

35 [0018] In another aspect of the present invention, the optical recording medium includes a substrate, a protection plate, and a plurality of information layers sandwiched between the substrate and the protection plate. The plurality of information layers are for recording information signals as a length of a mark and a length of a space between two marks. The mark is formed when irradiated by light received through the substrate. The plurality of information layers have a learning region provided for leading edge position learning for determining an optimum value of recording start position used for forming the leading edge of the mark, and trailing edge position learning for determining an optimum value of the recording end position for forming the trailing edge of the mark to be recorded.

[0019] Preferably, each of the plurality of information layers may have the learning region.

[0020] Preferably, the optimum value of the recording start position and that of the recording end position may include an information on information layer to be recorded, the length of mark, and the length of the space preceding and succeeding the marks.

40 [0021] Preferably, the plurality of information layers may have a recording region for recording the optimum values of the recording start position and recording end position obtained by the learning.

[0022] Preferably, it may further include another learning region for learning the leading edge position and the trailing edge position when the information layer at light incident side of the information layer to be recorded includes recorded data and when it does not include recorded data.

45 [0023] In a further aspect of the present invention, a recording method of optical recording medium for recording information signals in an information layer as a length of mark and a length of a space between two marks. The mark is formed when irradiated by light received through the information layer. The recording method includes setting a leading edge starting condition, setting a trailing edge starting condition, and recording information signals by forming marks when irradiated by light received through the information layer. The leading edge starting condition may be used to change a recording start position for forming a leading edge of the mark depending on the length of the mark, the length of the space preceding the mark, and the information layer to be recorded. The trailing edge starting condition may be used to change the recording end position for forming a trailing edge of the mark depending on the length of

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the mark, the length of the space succeeding the mark, and the information layer to be recorded. The information signal is recorded by forming marks when irradiated by light received through the information layer. This recording is performed after setting a leading edge starting condition and setting a trailing edge starting condition.

[0024] Preferably, the recording start end positions may be changed for recording depending on a recorded or unrecorded state of the upper information layer positioned at light incident side.

[0025] Preferably, if recorded state and unrecorded state coexist in the upper information layer, the recording start position may be set as a position between the recording start positions in unrecorded state and in recorded state, and the recording end position may be set as a position between the recording end positions in unrecorded state and in recorded state.

[0026] Preferably, the recording start position and recording end position may be corrected based on quantity of light reflected from the upper information layer positioned at light incident side of the information layer to be recorded.

[0027] In a still further aspect of the present invention, there is a recording method of optical recording medium for recording information signals as a length of a mark and a length of a space between two marks. The mark is formed when irradiated by light received. The recording method includes, prior to recording the information, learning a leading edge for determining an optimum value of recording start position for forming the leading edge of the mark, and a step of learning a trailing edge for determining an optimum value of recording end position for forming the trailing edge of the mark. Learning the leading edge for determining an optimum value of recording start position is performed by trial writing of the marks by changing the information layer to be recorded, the length of the mark, and the length of the space preceding the mark. Learning the trailing edge for determining an optimum value of recording end position is performed by trial writing of the marks by changing the information layer to be recorded, the length of the mark, and the length of the space succeeding the mark.

[0028] Preferably, it may further include selecting at least two information layer out of the plurality of information layers, executing leading edge learning and trailing edge learning, and calculating optimum values of the recording start position and the recording end position of information layer other than the information layers, on the basis of the learning results at the leading edge learning and trailing edge learning.

[0029] Preferably, the optimum value of the recording start position and the recording end positions obtained as a result of learning at the leading edge learning step and trailing edge learning step may be recorded in the plurality of information layers.

[0030] Preferably, the optimum value of the recording start position and the optimum value of the recording end position obtained as a result of learning at the leading edge learning step and trailing edge learning step may be recorded in a storage device provided in a recording apparatus for the optical recording medium.

[0031] Preferably, an information about the leading edge recording condition and the information about the trailing edge recording condition recorded in the management region may be used as initial values at the leading edge learning and at the trailing edge learning.

[0032] Preferably, the leading edge learning and trailing edge learning may be executed when detecting a change in recorded state of the upper information layer positioned at light incident side of the information layer to be recorded.

[0033] Preferably, the leading edge learning and trailing edge learning may be executed when detecting a change in the quantity of light reflected from the information layer to be recorded.

[0034] In a yet further aspect of the present invention, there is a recording apparatus of optical recording medium having a plurality of information layers for recording information signals as a length of a mark and a length of a space between two marks. The mark is formed when irradiated by light received through the information layer. The recording apparatus includes a layer detector circuit, a basic pulse generator, a data length detector, a pulse synthesizer, and a laser driver. The layer detector circuit detects which one of the plurality of information layers is an information layer to be recorded. The basic pulse generator generates a pulse of a predetermined width at a leading edge position of a mark to be formed, pulse at the middle of the mark, and pulse of a predetermined width at a trailing edge position of the mark. The data length detector is composed of a recording mark detector circuit detecting the length of the mark, a preceding space detector circuit detecting the length of a space preceding the mark, a succeeding mark detector circuit detecting the length of a mark succeeding the mark, and a succeeding space detector circuit detecting the length of a space succeeding the mark. The timing controller is composed of a leading edge pulse delay circuit generating a delayed leading edge pulse by setting the delay amount of the leading edge pulse from the output signals of the recording mark detector circuit, preceding space detector circuit and layer detector circuit, and a trailing edge pulse delay circuit generating a delayed trailing edge pulse by setting the delay amount of the trailing edge pulse from the output signals of the recording mark detector circuit, succeeding space detector circuit and layer detector circuit. The pulse synthesizer forms a recording pulse by synthesizing the delayed leading edge pulse signal, intermediate pulse signal, and delayed trailing edge pulse signal. The laser driver modulates the intensity of the beam on the basis of the recording pulse.

[0035] Preferably, it may further include a timing controller composed of a recording identifying circuit, a leading edge pulse delay circuit, and a trailing edge pulse delay circuit. The recording identifying circuit detects the recorded state

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of the upper information layer positioned at light incident side of the information layer to be recorded. The leading edge pulse delay circuit generates a delayed leading edge pulse by setting the delay amount of the leading edge pulse from the output signals of the recording mark detector circuit, the preceding space detector circuit, said layer detector circuit, and the recording identifying circuit. The trailing edge pulse delay circuit generates a delayed trailing edge pulse by setting the delay amount of the trailing edge pulse from the output signals of the recording mark detector circuit, the succeeding space detector circuit, the layer detector circuit, and the recording identifying circuit.

[0036] Preferably, it may further include selector selecting the delay amount of leading edge pulse and delay amount of trailing edge pulse, from the leading edge starting information and trailing edge starting information recorded in the management region, according to the identifying result of the layer detector circuit.

[0037] Preferably, it may further include selector selecting the delay amount of leading edge pulse and delay amount of trailing edge pulse, from the leading edge starting information and trailing edge starting information recorded in the management region, according to the identifying result of the recording identifying circuit.

[0038] Preferably, it may further include a detector detecting the quantity of reflected light of the information layer to be recorded. Then, the delay amount of leading edge pulse and delay amount of trailing edge pulse may be determined based on the detecting result obtained by the reflected light quantity detector.

[0039] Preferably, the layer detector circuit may detect the layer identifier for identifying the information layer.

[0040] Preferably, the recording identifying circuit may detect the recording identifier for distinguishing whether the information layer is in recorded state or in unrecorded state.

[0041] In a yet aspect of the present invention, there is a recording apparatus of optical recording medium having a plurality of information layers for recording information signals as a length of a mark and a length of a space between two marks. The mark is formed when irradiated by light received through the information layer. The recording apparatus includes a layer detector, a leading edge learning means, and a trailing edge learning means. The layer detector detects which one of the plurality of information layers is the information layer to be recorded. The leading edge learning means determines an optimum value of delay amount of leading edge pulse depending on a position of the information layer to be recorded, a length of a mark, and a length of a space preceding the mark. The trailing edge learning means determines an optimum value of delay amount of trailing edge pulse depending on the position of the information layer to be recorded, a length of the mark, and the length of a space succeeding a recording mark.

[0042] Preferably, it may further include operating means for operating the leading edge learning means and trailing edge learning means by selecting at least two information layers out of the plurality of information layers, and calculating the optimum value of recording start position and the optimum value of recording end position of other information layers from the operating results of the leading edge learning means and trailing edge learning means.

[0043] Preferably, it may further include learning result recording device recording the learning result information obtained from the leading edge learning means and trailing edge learning means on the optical recording medium.

[0044] Preferably, the leading edge learning means and trailing edge learning means may use the leading edge recording information and trailing edge recording information recorded in the management region as initial values.

[0045] Preferably, the leading edge learning means and trailing edge learning means may further calculate when detecting a change in the recorded or unrecorded state of each information layer.

[0046] Preferably, the leading edge learning means and trailing edge learning means may further calculate when detecting a change in the quantity of reflected light in the information layer to be recorded.

[0047] According to the present invention, in the optical recording medium having a plurality of information layers, in the case of mark length recording at high density, an independent recording condition can be set in each recording layer. Therefore, in all information layers, jitter of signals when recording marks are reproduced can be decreased, then the bit error rate of signals can be improved. As a result, the data recording density is notably improved, then the optical recording medium can be increased in capacity.

## 46 BRIEF DESCRIPTION OF THE DRAWINGS

[0048] The present invention will become readily understood from the following description of preferred embodiments thereof made with reference to the accompanying drawings, in which like parts are designated by like reference numeral and in which:

Fig. 1 is a block diagram showing a recording apparatus of optical recording medium in a first embodiment of the present invention;

Fig. 2 is a sectional view of optical recording medium in the first embodiment of the present invention;

Fig. 3 is a timing chart when forming recording marks on optical recording medium in the first embodiment of the present invention;

Fig. 4 is a block diagram showing a recording apparatus of optical recording medium in a second embodiment of the present invention;

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- Fig. 5 is a sectional view of optical recording medium in the second embodiment of the present invention; Fig. 6 is a sectional view of optical recording medium in a third embodiment of the present invention; Fig. 7 is a flowchart showing the learning procedure of recording pulse delay amount in a recording method of optical recording medium in the third embodiment of the present invention; Fig. 8 is a block diagram of a recording apparatus of optical recording medium in the third embodiment of the present invention; Fig. 9 is a conceptual diagram showing the relation of reproduced signal of recording mark, delay amount, and edge interval in the third embodiment of the present invention; Fig. 10 is a flowchart showing the learning procedure of recording pulse delay amount in a recording method of optical recording medium in a fourth embodiment of the present invention; and Fig. 11 is a sectional view of a conventional optical recording medium.

## BEST MODE FOR CARRYING OUT THE INVENTION

[0049] Referring now to the drawings, embodiments of the invention are described below.

## Embodiment 1

[0050] Fig. 2 is a sectional view showing an optical recording medium used in a first embodiment of the present invention. In Fig. 2, an optical recording medium 201 includes a plurality of information layers 203 (203a, 203b, 203c) formed on a substrate 202, and the individual information layers are separated by separation layers 204 (204a, 204b). In this embodiment, the multilayer recording layer is composed of, for example, three information layers 203.

[0051] The substrate 202 is made of polycarbonate or other resin material, or glass. On the surface of the substrate, various patterns are formed, such as guide tracks having a specific depth for tracking of beam, pits for address, or pits depending on the management information of the optical recording medium.

[0052] The material for composing the information layers 203a, 203b, 203c is available in two types, that is, the write-once type for recording only once, and the rewritable type for recording repeatedly. The write-once type recording materials include a material making use of phase change, such as Te-O and Te-O-Pd, that is, the signal is recorded by making use of difference in optical constant between amorphous state and crystalline state. Other is a recording material for recording by making use of change in the quantity of reflected light or quantity of transmitted light depending on diffraction by shape change of organic dye material of the like, or presence or absence of information layer. The rewritable type materials include a phase change material changing in phase between amorphous and crystalline state, and magneto-optical material making use of magneto-optical effect. As phase change material, such as Ge-Sb-Te or In-Sb-Te may be used. As magneto-optical material, for example, Tb-Fe-Co may be used.

[0053] Transparent separation layers 204a and 204b are formed among the information layers 203a, 203b, 203c. The separation layers 204a, 204b are required to be thin films small in light absorption at the wavelength of the irradiating light, and dielectric materials such as SiO<sub>2</sub>, ZnS, SiN, and AlN, or PMMA, polystyrene, and other resin materials may be used. The interval of the information layers 203a, 203b, 203c is set according to the allowable crosstalk of signals recorded in each information layer. On the information layer 203c laminated at the remotest position as seen from the light incident side, a protection plate 205 is provided to protect the information layer. The material of the protection plate 205 may be same as the material of the substrate 202, or metal or other flat plate.

[0054] In a data region 208 for recording information signals on the information layer 203, marks of various lengths are recorded at intervals of various spaces by assigning recording information in both mark length and space length.

[0055] A management region 206 is provided in a specific position of the optical recording medium 201. The management region 206 records, in each one of the information layers 203a, 203b, 203c, a starting information about leading edge recording condition and a starting information about trailing edge recording condition. The starting information about leading edge for varying the recording start position for forming the leading edge of the mark to be recorded, and depends on the length of the mark to be recorded and the length of the space preceding the mark. Then, the starting information about trailing edge recording condition varies the recording end position for forming the trailing edge of the mark to be recorded, and depends on the length of the mark to be recorded and the length of the space succeeding the mark. The form of the management region 206 may be pits, same recording mark shape as data region, barcode, or combination thereof. As a result, recording conditions of each information layer can be set depending on characteristics of information layer, and a stable recorded state can be obtained.

[0056] The management region 206 is provided on one specific information layer, preferably on the information layer 203a at the closest side to the light source from the light incident side. The light beam may be directed to the information layer 203a to reproduce the information recorded in the management region 206 of the information layer 203a without passing through other layers. Therefore, the information may be clearly reproduced.

[0057] Further on the information layers 203a, 203b, 203c, layer identifiers 207 recording the information for identi-

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fying the lamination position of each information layer provided at positions close to the data region 208 on the same layer. The form of the layer identifiers 207 may be pits, same recording mark shape as data region, barcode, or combination thereof. The layer identifiers 207 may also have an information of the address for managing the position information in the plane direction. In this case, the information layer to be recorded and the information of the radial position can be identified by the layer identifiers 207 at the same time.

[0058] Referring to a block diagram in Fig. 1 and a timing chart in Fig. 3, it is explained how an apparatus records an information in the optical recording medium 201 from an external apparatus. The data 6 (Fig. 3 (b)) is the information to be recorded at the timing synchronized with clock 53 (Fig. 3 (e)). The data 6 is fed in a leading edge pulse generating circuit 2, a burst gate generating circuit 3, and a trailing edge pulse generating circuit 4, which are combined to compose a basic pulse generator 1. In the leading edge pulse generator circuit 2, a leading edge pulse 40 (Fig. 3 (c)) having one period width of clock is generated in the leading edge of Hi period of data 6 (Fig. 3 (b)). In the burst gate generator circuit 3, a burst gate signal 41 (Fig. 3 (d)) is generated in a length shorter by three clocks than the clock length of the mark to be recorded in the middle position of the mark. However, if the mark length is three clocks or shorter, burst gate signal 41 is not generated. In the trailing edge pulse generating circuit 4, a trailing edge pulse 42 (Fig. 3 (e)) having one period of clock is generated in the trailing edge of Hi period of data 6.

[0059] In the embodiment, the input data 6 (Fig. 3 (b)) is data in a length of clock unit, and signals like (8-16) modulation signal having Hi period or Lo period having three periods or longer of the clock 53 (Fig. 3 (a)). Then, the data having the Hi period is recorded as a mark on the optical recording medium, and the data having the Lo period is recorded as a space between two marks. That is to say, mark and space length recording. To simplify the explanation, it is supposed to change the leading edge recording condition and trailing edge recording condition by detecting space length and mark length of 3T and 4T, respectively.

[0060] The data 6 is further fed in a preceding space detector circuit 7, a recorded mark detector circuit 8, and a succeeding space detector circuit 9, which are combined to compose a data length detector 5. The preceding space detector circuit 7 detects data of three-clock and four-clock width of Lo period of data 6, that is, 3T and 4T spaces, and sends a space length detection signal 43 to a leading edge starting position setting circuit 23. The recorded mark detector circuit 8 sends a recorded mark length detection signal 44, by detecting the length of the recorded mark, to the leading edge starting position setting circuit 23.

[0061] A laser beam 61 is irradiated to the management region 206 on the optical recording medium 201 through an optical pick 60, and a reflected light from the management region 206 is received and converted into an electric signal by a detector 62. Then, a detection signal 37 is outputted from the detector 62. An amplifier 71 amplifies the detection signal 37, and outputs a reproduction signal 75 to a leading edge recording information demodulation circuit 72, a trailing edge recording information demodulation circuit 73, and a layer detector circuit 74.

[0062] The leading edge recording information demodulation circuit 72 demodulates the leading edge recording information 38 for recording in each information layer included in the reproduction signal 75. The leading edge recording information 38 is stored respectively in first to third leading edge condition memory circuits 13, 14, 15 corresponding to the information layers.

[0063] Table 1 is an examples of leading edge memory information of the leading edge condition memory circuits 13, 14, 15. The first leading edge condition memory circuit 13 stores leading edge starting position parameters (layer information, preceding space length, recording mark length) of the information layer 203a, and leading edge starting position setting (address (setting value), delay time d1) corresponding to the parameters. Similarly, the second and third leading edge condition memory circuits 14, 15 store the leading edge starting position parameters of each layer and leading edge starting position setting corresponding to the parameter. In this way, the leading edge recording condition is stored in each leading edge condition memory circuit for each information layer.

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Table 1.

| Composition of leading edge Recording information |                                                 |                                 |                        |             |                                          |    |
|---------------------------------------------------|-------------------------------------------------|---------------------------------|------------------------|-------------|------------------------------------------|----|
| 5                                                 | Circuit Name                                    | Leading edge starting parameter |                        |             | Leading edge starting position condition |    |
|                                                   |                                                 | Information layer               | Precoding space length | Mark length |                                          |    |
| 10                                                | First leading edge condition memory circuit 13  | Information layer 203a          | 3T                     | 3T          | 11                                       | aa |
|                                                   |                                                 |                                 | 4T                     | 3T          | 12                                       | ab |
|                                                   |                                                 |                                 | 5T to 11T              | 3T          | 13                                       | ac |
|                                                   |                                                 |                                 | 3T                     | 4T          | 14                                       | ad |
|                                                   |                                                 |                                 | 4T                     | 4T          | 15                                       | ae |
|                                                   |                                                 |                                 | 5T to 11T              | 4T          | 16                                       | af |
|                                                   |                                                 |                                 | 3T                     | 5T to 11T   | 17                                       | ag |
|                                                   |                                                 |                                 | 4T                     | 5T to 11T   | 18                                       | ah |
|                                                   |                                                 |                                 | 5T to 11T              | 5T to 11T   | 19                                       | ai |
|                                                   |                                                 |                                 | 3T                     | 3T          | 21                                       | ba |
| 20                                                | Second leading edge condition memory circuit 14 | Information layer 203b          | 4T                     | 3T          | 22                                       | bb |
|                                                   |                                                 |                                 | 5T to 11T              | 3T          | 23                                       | bc |
|                                                   |                                                 |                                 | 3T                     | 4T          | 24                                       | bd |
|                                                   |                                                 |                                 | 4T                     | 4T          | 25                                       | be |
|                                                   |                                                 |                                 | 5T to 11T              | 4T          | 26                                       | bf |
|                                                   |                                                 |                                 | 3T                     | 5T to 11T   | 27                                       | bg |
|                                                   |                                                 |                                 | 4T                     | 5T to 11T   | 28                                       | bh |
|                                                   |                                                 |                                 | 5T to 11T              | 5T to 11T   | 29                                       | bi |
|                                                   |                                                 |                                 | 3T                     | 3T          | 31                                       | ca |
|                                                   |                                                 |                                 | 4T                     | 3T          | 32                                       | cb |
| 30                                                | Third leading edge condition memory circuit 15  | Information layer 203c          | 5T to 11T              | 3T          | 33                                       | cc |
|                                                   |                                                 |                                 | 3T                     | 4T          | 34                                       | cd |
|                                                   |                                                 |                                 | 4T                     | 4T          | 35                                       | ce |
|                                                   |                                                 |                                 | 5T to 11T              | 4T          | 36                                       | cf |
|                                                   |                                                 |                                 | 3T                     | 5T to 11T   | 37                                       | cg |
|                                                   |                                                 |                                 | 4T                     | 5T to 11T   | 38                                       | ch |
|                                                   |                                                 |                                 | 5T to 11T              | 5T to 11T   | 39                                       | ci |
|                                                   |                                                 |                                 | 3T                     | 3T          | 40                                       | ca |
|                                                   |                                                 |                                 | 4T                     | 3T          | 41                                       | cb |
|                                                   |                                                 |                                 | 5T to 11T              | 3T          | 42                                       | cc |

[0064] Similarly, the trailing edge recording information demodulation circuit 73 demodulates the trailing edge recording information 47 for recording in each information layer included in the reproduction signal 75. The trailing edge recording information 47 is stored respectively in first to third trailing edge condition memory circuits 18, 19, 20 corresponding to the information layers.

[0065] Table 2 shows an examples of trailing edge memory information of the trailing edge condition memory circuits 18, 19, 20. The first trailing edge condition memory circuit 18 stores trailing edge starting position parameters (layer information, succeeding space length, recording mark length) of the information layer 203a, and trailing edge starting position setting (address (setting value), delay time d2) corresponding to the parameters. Similarly, the second and third trailing edge condition memory circuits 19, 20 store the trailing edge starting position parameters of each layer

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and trailing edge starting position setting corresponding to the parameters. In this way, the trailing edge recording condition is stored in each trailing edge condition memory circuit for each information layer.

Table 2.

| Circuit Name                                     | Trailing edge starting position parameter |             |                         | Trailing edge starting position condition |                    |
|--------------------------------------------------|-------------------------------------------|-------------|-------------------------|-------------------------------------------|--------------------|
|                                                  | Information layer                         | Mark length | Succeeding space length | Address                                   | Delay time d2 (ns) |
| First trailing edge condition memory circuit 16  | Information layer 203a                    | 3T          | 3T                      | 41                                        | da                 |
|                                                  |                                           | 3T          | 4T                      | 42                                        | db                 |
|                                                  |                                           | 3T          | 5T to 11T               | 43                                        | dc                 |
|                                                  |                                           | 4T          | 3T                      | 44                                        | dd                 |
|                                                  |                                           | 4T          | 4T                      | 45                                        | de                 |
|                                                  |                                           | 4T          | 5T to 11T               | 46                                        | df                 |
|                                                  |                                           | 5T to 11T   | 3T                      | 47                                        | dg                 |
|                                                  |                                           | 5T to 11T   | 4T                      | 48                                        | dh                 |
|                                                  |                                           | 5T to 11T   | 5T to 11T               | 49                                        | di                 |
|                                                  |                                           | 3T          | 3T                      | 51                                        | ea                 |
| Second trailing edge condition memory circuit 19 | Information layer 203b                    | 3T          | 4T                      | 52                                        | eb                 |
|                                                  |                                           | 3T          | 5T to 11T               | 53                                        | ec                 |
|                                                  |                                           | 4T          | 3T                      | 54                                        | ed                 |
|                                                  |                                           | 4T          | 4T                      | 55                                        | ee                 |
|                                                  |                                           | 4T          | 5T to 11T               | 56                                        | ef                 |
|                                                  |                                           | 5T to 11T   | 3T                      | 57                                        | eg                 |
|                                                  |                                           | 5T to 11T   | 4T                      | 58                                        | eh                 |
|                                                  |                                           | 5T to 11T   | 5T to 11T               | 59                                        | ei                 |
|                                                  |                                           | 3T          | 3T                      | 61                                        | fa                 |
|                                                  |                                           | 3T          | 4T                      | 62                                        | fb                 |
| Third trailing edge condition memory circuit 20  | Information layer 203c                    | 3T          | 5T to 11T               | 63                                        | fc                 |
|                                                  |                                           | 4T          | 3T                      | 64                                        | fd                 |
|                                                  |                                           | 4T          | 4T                      | 65                                        | fe                 |
|                                                  |                                           | 4T          | 5T to 11T               | 66                                        | ff                 |
|                                                  |                                           | 5T to 11T   | 3T                      | 67                                        | fg                 |
|                                                  |                                           | 5T to 11T   | 4T                      | 68                                        | fh                 |
|                                                  |                                           | 5T to 11T   | 5T to 11T               | 69                                        | fi                 |

[0086] Consequently, by irradiated the laser beam 61 to the layer identifier 207 provided on each information layer 203, a desired information layer is identified.

[0087] The layer detector circuit 74 demodulates the information layer identifying information of the layer identifier 207 from the reproduction signal 75, and identifies which the information layer focused by the laser beam 61 is any one of 203a, 203b, 203c, and outputs an identifying signal 39 for information layer. According to the identifying signal 39 for information layer, a leading edge condition changeover switch 16 selects one leading edge condition memory circuit (13, 14, 15) corresponding to the information layer to be recorded, and outputs the stored leading edge recording

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information 45.

[0066] The leading edge starting position setting circuit 23 determines the parameter, on the basis of the preceding space length detection signal 43 and recording mark length detection signal 44, with respect to the leading edge recording information 45, and determines the delay time of the leading edge pulse from this parameter. For example, as explained in Table 1, when recording in the information layer 203a, the first leading edge condition memory circuit 13 is selected, and the contents of the addresses (setting values) 11 to 19 are determined, that is, the values of  $a_0$  to  $a_1$  as delay time  $d_1$  depending on the preceding space length and recording mark length. When recording in the information layer 203b, the second leading edge condition memory circuit 14 is selected, and the contents of the addresses (setting values) 21 to 29 are determined, that is, the values of  $b_0$  to  $b_1$  as delay time  $d_1$  depending on the preceding space length and recording mark length. Further, when recording in the information layer 203c, the third leading edge condition memory circuit 15 is selected, and the contents of the addresses (setting values) 31 to 39 are determined, that is, the values of  $c_0$  to  $c_1$  as delay time  $d_1$  depending on the preceding space length and recording mark length. Herein, the delay time  $d_1$  is supposed to be a positive value, but a negative value may be also included.

[0069] The leading edge pulse delay circuit 24 delays the leading edge pulse 40 (Fig. 3 (c)) according to a delay signal 49 outputted from the leading edge position setting circuit 23, and outputs a delayed leading edge pulse 50 (Fig. 3 (f)). Thus, depending on the information layer for forming recording marks, recording mark length, and preceding space length, the leading edge pulse delay circuit 24 may vary the delayed leading edge pulse 50 in the delayed amount.

[0070] Similarly, the trailing edge starting position setting circuit 25 determines the parameter, on the basis of the trailing edge recording information 46, recording mark length detection signal 44, and succeeding space length detection signal 45, and determines the delay time of the trailing edge pulse from this parameter. For example, as explained in Table 2, when recording in the information layer 203a, the first trailing edge condition memory circuit 18 is selected, and the contents of the addresses (setting values) 11 to 19 are determined, that is, the values of  $d_0$  to  $d_1$  as delay time  $d_2$  depending on the succeeding space length and recording mark length. When recording in the information layer 203b, the second trailing edge condition memory circuit 19 is selected, and the contents of the addresses (setting values) 21 to 29 are determined, that is, the values of  $e_0$  to  $e_1$  as delay time  $d_2$  depending on the succeeding space length and recording mark length. Further, when recording in the information layer 203c, the third trailing edge condition memory circuit 20 is selected, and the contents of the addresses (setting values) 31 to 39 are determined, that is, the values of  $f_0$  to  $f_1$  as delay time  $d_2$  depending on the succeeding space length and recording mark length. Herein, the delay time  $d_2$  is supposed to be a positive value, but a negative value may be also included.

[0071] The trailing edge pulse delay circuit 26 delays the trailing edge pulse 42 (Fig. 3 (e)) according to a delay signal 51 outputted from the trailing edge position setting circuit 25, and outputs a delayed trailing edge pulse 52 (Fig. 3 (g)). Thus, depending on the information layer for forming recording marks, recording mark length, and succeeding space length, the trailing edge pulse delay circuit 26 may vary the delayed trailing edge pulse 52 in the delayed amount.

[0072] On the other hand, an AND gate 29 operates logical product AND between burst gate signal 41 (Fig. 3 (d)) and clock 53 (Fig. 3 (a)), and outputs an intermediate pulse 54 (Fig. 3 (h)). However, if the mark length is 3T or shorter, burst gate is not generated.

[0073] Next, an OR gate 30 operates logical sum OR of delayed leading edge pulse 50, delayed trailing edge pulse 52, and intermediate pulse 54, and generates a recording pulse 28 (Fig. 3 (l)).

[0074] A laser drive unit 31 drives a laser unit 32 according to the recording pulse 28. The laser unit 32 emits light with a bias power by a driving current by a bias current source 33. Parallel to this bias current source 33, a recording current source 34 is connected, and when the current of the recording current source 34 is turned on or off by a switch 35, the laser unit 32 is switched between the peak power and bias power. When the recording pulse 28 is entered by the switch 35, the light emission strength of the laser unit 32 is modulated between the peak power and bias power, and a luminescence waveform (Fig. 3 (j)) is outputted. When this modulated light is emitted to the information layer on the optical recording medium 201, mark and space (Fig. 3 (k)) are formed.

[0075] In this operation, the optical recording apparatus of the embodiment can record the mark recording start position and recording end position as marks and spaces corresponding to desired data in each desired information layer. Accordingly, it can decrease mark distortion due to heat interference caused by heating, cooling condition, and enhancement of density difference in each information layer, and hence decrease the jitter of signal when reproducing the recording mark of each recording layer. As a result, in each information layer of the optical recording medium having a plurality of information layers, signals can be recorded at small bit error rate.

[0076] In this embodiment, the information layer to be recorded is identified by the information of the layer identifier 207, but the information layer may be also identified by the difference in the quantity of reflected light from each information layer. In this case, the layer detector circuit 74 can identify which information layer is to be recorded by the magnitude of the signal level of the detection signal 37, so that the structure of the layer detector circuit 74 may be simplified.

[0077] The information layer may be also identified by the address having the position information exposed to light. In this case, a uniform address is set in all plurality of information layers. For example, a different address may be set

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in each information layer in the sector address provided in each sector in the information layer. In the demodulated address information, parallelising between the address and information layer, the information layer as reproducing can be specified.

[0078] In the embodiment, to simplify the explanation, three information layers 203 are laminated, but when the number of layers is increased or decreased, the addresses (setting values) may be set corresponding to the number of layers.

[0079] In this example, when the length of space is 5T or longer, the delay amount of the space is set to the specific value. If a space having a length longer than 5T will be used, any delay of leading edge pulse and trailing edge pulse for all space lengths are controlled so that the bit error rate will be improved.

[0080] By detecting the length of recorded mark and the length of space preceded the mark, the delay is controlled according to the result of detection. Furthermore, detecting the preceding and succeeding mark lengths, the length of space preceding the preceding mark, and the length of space succeeding the succeeding mark, then the delay of leading edge pulse and trailing edge pulse may be controlled according to this detection result. Thus, by preliminarily detecting the preceding and succeeding mark lengths or preceding and succeeding space lengths in a wider range, the bit error rate may be further improved.

[0081] By delay control of the leading edge pulse position and trailing edge pulse position, the recording start position and recording end position of the recording marks are determined, but by fixing the succeeding edge position of the leading edge pulse and delaying the leading edge pulse start edge position, the start edge position of the trailing edge pulse may be fixed and the trailing edge pulse end edge position may be delayed and controlled.

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## Embodiment 2

[0082] A second embodiment of the present invention is explained below. When recording in second and subsequent information layers from the light source side, the transmissivity of the information layer varies depending on whether the information layer of the light source layer is in recorded state or unrecorded state. Accordingly, the intensity of the laser beam reaching these information layers varies, and the recording condition at the leading edge and trailing edge may be changed. By contrast, in this embodiment, the independent leading edge recording condition and trailing edge recording condition are set depending on the recorded state of each layer closer to the light source side than the information layer to be recorded.

[0083] Fig. 5 is a sectional view of an optical recording medium. An optical recording medium 501 includes a substrate 202, an information layer 203, a separation layer 204, a protection plate 205, and a layer identifier 207, and the same one as in the first embodiment may be used. The information layer 203 includes recording identifiers 502 (502a, 502b, 502c) identifying whether the recorded state or unrecorded state of the information layers 203a, 203b, 203c. The form of the recording identifier 502 may be same recording mark shape as data region, barcode, or combination thereof.

[0084] The management region 206 includes, in addition to those in the first embodiment, the information of leading edge recording condition and trailing edge recording condition, depending on the recorded state of the information layer at light incident side of each information layer. That is, the information layer 203b has the information relating to two kinds of recording condition of the information layer 203a, and the information layer 203c has the information relating to four kinds of recording condition of the information layers 203a, 203b.

[0085] Referring to a block diagram of Fig. 4, a configuration of an apparatus for recording information in the optical recording medium 501 is explained. Fig. 4 is similar to the block diagram of Fig. 1, except that the configuration of the leading edge condition selector, trailing edge condition selector, and demodulation unit is different.

[0086] A detection signal 37 from the optical recording medium 501 is amplified in an amplifier 71, and a reproduction signal 75 is outputted. A leading edge recording condition demodulation circuit 432 demodulates the leading edge recording information 440 contained in the reproduction signal 75. The leading edge recording information 440 is classified in seven types according to the information layer and recording condition. The leading edge recording information 440 having the seven types are respectively outputted in first leading edge condition memory circuit 402 to seventh leading edge condition memory circuit 408. Table 3 shows the contents recorded in the first leading edge condition memory circuit 402 to seventh leading edge condition memory circuit 408.

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Table 3.

| Composition of leading edge recording signal |                                                   |                                          |                                                  |                        |                                          |           |
|----------------------------------------------|---------------------------------------------------|------------------------------------------|--------------------------------------------------|------------------------|------------------------------------------|-----------|
|                                              | Circuit name                                      | Leading edge starting position parameter |                                                  |                        | Leading edge starting position condition |           |
|                                              |                                                   | Information layer                        | Recorded state of upper information layer        | Preceding space length | Mark length                              | Address   |
| 5                                            | First leading edge condition memory circuit 402   | Information layer 203a                   | -                                                | 3T                     | 3T                                       | aa        |
|                                              |                                                   |                                          |                                                  | 4T                     | 3T                                       | ab        |
|                                              |                                                   |                                          |                                                  | 5T to 11T              | 3T                                       | ac        |
|                                              |                                                   |                                          |                                                  | 3T                     | 4T                                       | ad        |
|                                              |                                                   |                                          |                                                  | 4T                     | 4T                                       | ae        |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 4T                                       | af        |
|                                              |                                                   |                                          |                                                  | 3T                     | 5 to 11T                                 | ag        |
|                                              |                                                   |                                          |                                                  | 4T                     | 5 to 11T                                 | ah        |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5 to 11T                                 | ai        |
| 10                                           | Second leading edge condition memory circuit 403  | Information layer 203b                   | Information layer 203a unrecorded                | 3T                     | 3T                                       | bba       |
|                                              |                                                   |                                          |                                                  | 4T                     | 3T                                       | bbs       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | bis       |
| 15                                           | Third leading edge condition memory circuit 404   | Information layer 203b                   | Information layer 203a recorded                  | 3T                     | 3T                                       | bab       |
|                                              |                                                   |                                          |                                                  | 4T                     | 3T                                       | bbb       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | bib       |
|                                              |                                                   |                                          |                                                  | 3T                     | 3T                                       | caa       |
| 20                                           | Fourth leading edge condition memory circuit 405  | Information layer 203c                   | Information layers 203a, 203b unrecorded         | 4T                     | 3T                                       | cba       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | cia       |
|                                              |                                                   |                                          |                                                  | 3T                     | 3T                                       | cab       |
| 25                                           | Fifth leading edge condition memory circuit 406   | Information layer 203c                   | Information layers 203a, 203b recorded           | 4T                     | 3T                                       | cbb       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | cib       |
|                                              |                                                   |                                          |                                                  | 3T                     | 3T                                       | cbc       |
| 30                                           | Sixth leading edge condition memory circuit 407   | Information layer 203c                   | Information layer 203a unrecorded, 203b recorded | 4T                     | 3T                                       | cbc       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | cic       |
|                                              |                                                   |                                          |                                                  | 3T                     | 3T                                       | cac       |
| 35                                           | Seventh leading edge condition memory circuit 408 | Information layer 203c                   | Information layer 203a recorded, 203b unrecorded | 4T                     | 3T                                       | cbd       |
|                                              |                                                   |                                          |                                                  | (omitted)              | (omitted)                                | (omitted) |
|                                              |                                                   |                                          |                                                  | 5~11T                  | 5~11T                                    | cid       |
|                                              |                                                   |                                          |                                                  | 3T                     | 3T                                       | cad       |

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[0087] The contents recorded in the first leading edge condition memory circuit 402 are the leading edge starting position setting (address (setting value), delay time d1) corresponding to the leading edge starting position parameters (layer information, preceding space length, recording mark length) when recording in the information layer 203a. The contents recorded in the second leading edge condition memory circuit 403 are the leading edge starting position setting (address (setting value), delay time d1) corresponding to the leading edge starting position parameters (layer information, preceding space length, recording mark length) when recording in the information layer 203b and when the incident side recording layer 203a is in unrecorded state. The contents recorded in the third leading edge condition memory circuit 404 are the leading edge starting position setting (address (setting value), delay time d1) corresponding to the leading edge starting position parameters (layer information, preceding space length, recording mark length) when recording in the information layer 203b and when the incident side recording layer 203a is in recorded state.

[0088] Further, the contents recorded in the fourth to seventh leading edge condition memory circuits 405, 406, 407, 408 are the leading edge starting position setting (address (setting value), delay time d1) corresponding to the leading edge starting position parameters (layer information, preceding space length, recording mark length) when recording in the information layer 203c and in four states, that is, when the two recording layers 203a, 203b positioned at light incident side are in recorded state and unrecorded state, respectively.

[0089] Next, by emitting light to the recording identifier 502 on the optical recording medium 501, the reproduction signal 75 is outputted to the recording identifying circuit 434. The recording identifying circuit 434 demodulates the recording identifying information from the reproduction signal 75, and identifies whether the information layers 203a, 203b are in recorded state or unrecorded state, and outputs a recording identifying signal 442. For example, when the information layer to be recorded is the information layer 203b, the second layer leading edge recording condition changeover switch 409 selects either the leading edge condition memory circuit 403 or 404 according to the recording identifying signal 442, and outputs leading edge recording information 443.

[0090] When the information layer to be recorded is the information layer 203c, the third layer leading edge recording condition changeover switch 410 selects any one of the leading edge condition memory circuits 405 to 408 according to the recording identifying signal 442, and outputs leading edge recording information 444. The leading edge condition changeover switch 18 operates same as in the first embodiment, and any one of the first layer leading edge information 445, second layer leading edge information 443, and third layer leading edge information 444 is selected according to the information layer identifying signal, and leading edge information 45 is outputted. By this operation, depending on the recorded state of the information layer to be recorded and each information layer at light incident side, a delayed leading edge pulse varied in the delay amount can be outputted.

[0091] Similarly, a trailing edge recording condition demodulation circuit 433 demodulates the trailing edge recording information 441 contained in the reproduction signal 75. The trailing edge recording information 441 are classified in seven types by the information layer and recording condition. The trailing edge recording information 441 are outputted in first trailing edge condition memory circuit 420 to seventh trailing edge condition memory circuit 425. Table 4 shows the contents recorded in the first trailing edge condition memory circuit 420 to seventh leading edge condition memory circuit 426.

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Table 4.

| Composition of trailing edge recording signal           |                                           |                                                            |             |                         |                                           |                    |
|---------------------------------------------------------|-------------------------------------------|------------------------------------------------------------|-------------|-------------------------|-------------------------------------------|--------------------|
| Circuit name                                            | Trailing edge starting position parameter |                                                            |             |                         | Trailing edge starting position condition |                    |
|                                                         | Information layer                         | Recorded state of upper information layer                  | Mark length | Succeeding space length | Address                                   | Delay time d2 (ns) |
| First trailing edge condition memory circuit 420        | Information layer 203a                    |                                                            | 3T          | 3T                      | 11                                        | da                 |
|                                                         |                                           |                                                            | 3T          | 4T                      | 12                                        | db                 |
|                                                         |                                           |                                                            | 3T          | 5T to 11T               | 13                                        | dc                 |
|                                                         |                                           |                                                            | 4T          | 3T                      | 14                                        | dd                 |
|                                                         |                                           |                                                            | 4T          | 4T                      | 15                                        | de                 |
|                                                         |                                           |                                                            | 4T          | 5 to 11T                | 16                                        | df                 |
|                                                         |                                           |                                                            | 5T to 11T   | 3T                      | 17                                        | dg                 |
|                                                         |                                           |                                                            | 5T to 11T   | 4T                      | 18                                        | dh                 |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 19                                        | di                 |
|                                                         |                                           |                                                            | 3T          | 3T                      | 21                                        | eaa                |
| Second trailing edge condition memory circuit 421       | Information layer 203b                    | Information layer 203a unrecorded                          | 4T          | 3T                      | 22                                        | eba                |
|                                                         |                                           |                                                            | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 29                                        | eia                |
|                                                         |                                           |                                                            | 3T          | 3T                      | 21                                        | eab                |
| Third trailing edge condition memory circuit 422        | Information layer 203b                    | Information layer 203a recorded                            | 4T          | 3T                      | 22                                        | ebb                |
|                                                         |                                           |                                                            | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 29                                        | eib                |
|                                                         |                                           |                                                            | 3T          | 3T                      | 31                                        | faa                |
| Fourth trailing edge condition memory circuit 423       | Information layer 203c                    | Information layers 203a, 203b unrecorded                   | 4T          | 3T                      | 32                                        | fba                |
|                                                         |                                           |                                                            | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 39                                        | fia                |
|                                                         |                                           |                                                            | 3T          | 3T                      | 31                                        | fab                |
| Fifth trailing edge condition memory circuit 424        | Information layer 203c                    | Information layers 203a, 203b recorded                     | 4T          | 3T                      | 32                                        | fbb                |
|                                                         |                                           |                                                            | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 39                                        | fib                |
|                                                         |                                           |                                                            | 3T          | 3T                      | 31                                        | fbc                |
| Sixth trailing edge condition memory circuit 425<br>425 | Information layer 203c                    | Information layer 203a unrecorded<br>203b recorded<br>203b | 4T          | 3T                      | 32                                        | fbd                |
|                                                         |                                           |                                                            | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                         |                                           |                                                            | 5T to 11T   | 5T to 11T               | 39                                        | fcd                |
|                                                         |                                           |                                                            | 3T          | 3T                      | 31                                        | fac                |

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Table 4. (continued)

| Composition of trailing edge recording signal      |                        | Trailing edge starting position parameter        |             |                         | Trailing edge starting position condition |                    |
|----------------------------------------------------|------------------------|--------------------------------------------------|-------------|-------------------------|-------------------------------------------|--------------------|
| Circuit name                                       | Information layer      | Recorded state of upper Information layer        | Mark length | Succeeding space length | Address                                   | Delay time d2 (ns) |
| Seventh trailing edge condition memory circuit 426 | Information layer 203c | Information layer 203a recorded, 203b unrecorded | 3T          | 3T                      | 31                                        | fad                |
|                                                    |                        |                                                  | 4T          | 3T                      | 32                                        | fbd                |
|                                                    |                        |                                                  | (omitted)   | (omitted)               | (omitted)                                 | (omitted)          |
|                                                    |                        |                                                  | 5T to 11T   | 5T to 11T               | 39                                        | fid                |

[0092] The contents recorded in the first trailing edge condition memory circuit 420 are the trailing edge starting position setting (address (setting value), delay time d2) corresponding to the trailing edge starting position parameters (layer information, succeeding space length, recording mark length) when recording in the information layer 203a. The contents recorded in the second trailing edge condition memory circuit 421 are the trailing edge starting position setting (address (setting value), delay time d2) corresponding to the trailing edge starting position parameters (layer information, succeeding space length, recording mark length) when recording in the information layer 203b and when the incident side recording layer 203a is in unrecorded state. The contents recorded in the third trailing edge condition memory circuit 422 are the trailing edge starting position setting (address (setting value), delay time d2) corresponding to the trailing edge starting position parameters (layer information, succeeding space length, recording mark length) when recording in the information layer 203b and when the incident side recording layer 203a is in recorded state.

[0093] Further, the contents recorded in the fourth to seventh trailing edge condition memory circuits 423, 424, 425 are the trailing edge starting position setting (address (setting value), delay time d2) corresponding to the trailing edge starting position parameters (layer information, succeeding space length, recording mark length) when recording in the information layer 203c and in four states, that is, when the two recording layers 203a, 203b positioned at light incident side are in recorded state and unrecorded state, respectively.

[0094] Next, by emitting light to the recording Identifier 502 on the optical recording medium 501, the reproduction signal 75 is outputted to the recording Identifying circuit 433. The recording identifying circuit 434 demodulates the recording identifying information from the reproduction signal 75, and identifies whether the information layers 203a, 203b are in recorded state or unrecorded state, and outputs a recording identifying signal 441. For example, when the information layer to be recorded is the information layer 203b, the second layer trailing edge recording condition change-over switch 427 selects either the trailing edge condition memory circuit 421 or 422 according to the recording identifying signal 441, and outputs trailing edge recording information 451.

[0095] When the information layer to be recorded is the information layer 203c, the third layer trailing edge recording condition change-over switch 428 selects any one of the trailing edge condition memory circuits 423 to 426 according to the recording identifying signal 441, and outputs trailing edge recording information 452. The trailing edge condition change-over switch 21 operates same as in the first embodiment, and any one of the first layer trailing edge information 453, second layer trailing edge information 451, and third layer trailing edge information 452 is selected according to the information layer identifying signal 39, and trailing edge information 48 is outputted. By this operation, depending on the recorded state of the information layer to be recorded and each information layer at light incident side, a delayed trailing edge pulse varied in the delay amount can be outputted.

[0096] By this operation, the optical recording medium of the embodiment can record the recording start position and recording end position of the mark depending on the information layer to be recorded, recorded state of the upper information layer at the incident side, recording mark length, and length of space preceding and succeeding the mark. Accordingly, it can compensate for the difference in the light irradiation condition caused by recorded state of the information layer at the incident side, and the jitter of the signal by reproducing the recording mark is decreased. As a result, signals of small bit error rate can be recorded in each information layer of the optical recording medium having a plurality of information layers.

[0097] The recording identifying circuit 434 may be also designed to output the recording identifying information according to the level of the quantity of reflected light from the information layer at the light source side by estimating the quantity of reflected light from the reproduction signal when reproducing the information layer to be recorded. If

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recorded region and unrecorded region coexist in a passing range of incident beam of information layer at the light source side of the information layer to be recorded, the correction rate of the delay amount of the leading edge pulse and trailing edge pulse can be estimated from the level of the quantity of reflected light.

- [0098] When limited to a method of recording signals sequentially from the innermost information layer with respect to the light source, the recording conditions of leading edge and trailing edge of each information layer can be simplified to two types.

## Embodiment 3

- [0099] In order to correct the recording condition difference due to difference between optical recording media or difference between optical recording apparatuses, this embodiment proposes a method of determining optimum values by learning the leading edge position and trailing edge position of recording pulse in each information layer.

[0100] Fig. 8 is a diagram of sectional view of optical recording medium. Learning regions 601 are provided in the information layers 203a, 203b, 203c for determining the optimum values of delay amount of leading edge pulse and trailing edge pulse. A configuration of an apparatus for recording information in an optical recording medium 501 is shown in Fig. 8, in which a leading edge delay amount change circuit 801 is added to the leading edge pulse delay circuit 24 of the timing controller 22 in Fig. 1, and a trailing edge delay amount change circuit 802 is added to the trailing edge pulse delay circuit 25.

[0101] Referring to a flowchart in Fig. 7, we will explain the learning procedure of delay amount of leading edge pulse and trailing edge pulse in each information layer in the optical recording apparatus. At beam emitting step 701, a laser beam is focused on the learning region 601 on the information layer 203 for learning. At leading edge delay amount change step 702, the leading edge delay amount change circuit 801 outputs a delay signal 49 changing discontinuously the delay amount depending on the combination of the recording mark and the space preceding the mark. The leading edge pulse 704 outputs a delayed leading edge pulse 50 according to the delay signal 49.

[0102] At trailing edge delay amount change step 703, the trailing edge delay amount change circuit 802 outputs a delay signal 51 changing discontinuously the delay amount depending on the combination of the recording mark and the space succeeding the mark. The trailing edge pulse delay circuit 25 outputs a delayed trailing edge pulse 52 according to the delay signal 51. At recording step 704, the laser beam modulated by the recording pulses according to the delayed leading edge pulse 50 and delayed trailing edge pulse 52 discontinuously changed is emitted on the information layer 203, and recording marks are formed. At reproducing step 705, the edge interval of the recording marks and space reproducing signal formed at step 704 is measured.

[0103] Figs. 9A to 9C are diagrams showing the relation of the reproduced signal of recording mark, delay amount, and edge interval. Fig. 9A shows a reproduced signal waveform of recording mark. For example, on the basis of the preceding edge position of 6T mark, the preceding edge interval of 41 mark is measured. The preceding edge position of 4T mark is recorded by changing discontinuously at step 704, and the leading edge interval varies corresponding to the delay amount of the leading edge pulse (Fig. 9B). Further, on the basis of the succeeding edge position of 8T mark, the succeeding edge interval of 47 mark is measured. The succeeding edge position of 4T mark is recorded by changing discontinuously at step 704, and the trailing edge interval varies corresponding to the delay amount of the trailing edge pulse (Fig. 9C).

[0104] Identifying step 706 is to determine the optimum delay amounts of leading edge pulse and trailing edge pulse so that the preceding edge interval and succeeding edge interval of reproduction signal may be optimum length as shown in Figs. 9B and 9C. Step 706 is followed by delay amount setting step 707 of storing the leading edge position set value of the learned information layer in each leading edge condition memory circuit in the leading edge condition selector 12 in Fig. 4 explained in the first embodiment. Similarly, the trailing edge position set value of the learned information layer is stored in each trailing edge condition memory circuit in the trailing edge condition selector 17.

[0105] The process from step 701 to step 707 is repeatedly learned in the existing information layers. After learning from step 701 to step 707, at learning value recording step 709, the information of leading edge position set value and trailing edge position set value stored at step 707 is recorded in the management region on the optical recording medium 501. After step 709, the optical recording apparatus is in ready state waiting for recording.

[0106] In the procedure shown in Fig. 7, in each information layer of the optical recording medium 501 having a plurality of information layers 203, the leading edge information or trailing edge information may be set to the optimum value corresponding to the combination of the recording mark, immediately preceding space and immediately succeeding space.

[0107] It is preferred to record the recommended condition determined in the manufacturing process of the optical recording medium in the management region 206 on the optical recording medium 501. In this case, preferably, the starting value of the delay amount at delay amount change step 702 should be used as the leading edge starting position set value and trailing edge starting position set value recorded in the management region 206. In this case, learning is started from the interval closer to the optimum edge interval, and the learning time can be shortened by

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limiting the changing range of the delay amount.

[0108] In this case, the information of the leading edge position set value and trailing edge position set value stored at delay amount setting step 707 is recorded on the optical recording medium, and in this case, Furthermore, when the optical recording medium 501 is loaded again in the optical recording apparatus, by setting the delay amount of the leading edge position and delay amount of the trailing edge position according to the recorded leading edge information and trailing edge information, it is possible to record nearly at the optimum edge interval. As a result, the learning time is further shortened, or may be even eliminated.

[0109] In this learning, moreover, by executing in the information layer at the closest side to the light source and in the information layer at the remotest side from the light source, learning in other information layers can be omitted. In this case, learning results in other information layers may be set by interpolating from the learning results of the information layers that actually carried out the learning step. In this case, it is not needed to execute learning in all information layers, and the learning time can be shortened.

[0110] This learning may be also executed depending whether the information layer of the light incident side of the information layer to be recorded is in recorded state or unrecorded state. In this case, depending on the recorded state of the information layer at light incident side, the intensity of the laser beam reaching the subsequent information layers varies, and the recording condition of each information layer changes. Since the delay amount is learned corresponding to the combination of recorded state of each information layer, optimum leading edge position set value and trailing edge position set value corresponding to the recorded state of each information layer can be set, so that more accurate learning is possible.

[0111] After recording and learning corresponding to the recorded state of the information layer closer to the light source than the information layer to be recorded, if the quantity of reflected light of the information layer to be recorded is changed, learning may be executed again. Due to change in recorded state of information layer at light incident side, a recorded state region and an unrecorded state region may coexist, and the intensity of the laser beam reaching the subsequent information layers may be changed. In such a case, too, optimum leading edge position set value and trailing edge position set value corresponding to the intensity of the laser beam reaching the information layer can be set, so that further accurate learning is possible.

## Embodiment 4

[0112] A recording method of optical recording medium according to a fourth embodiment of the present invention is explained. In this recording method of optical recording medium, as compared with the recording method of optical recording medium in the third embodiment, it includes an additional step, that is, learning step is executed in selected information layers out of the plurality of information layers, and optimum values of recording start position and recording end position about the information layers skipping the learning step are calculated on the basis of the obtained learning results. According to this recording method of optical recording medium, the learning step is executed only in selected information layers, not in all information layers, and the required time for learning step can be shortened.

[0113] In this optical recording medium having a plurality of information layers, we will explain the reason why the optimum delay amount of other information layers can be calculated from the learning results of the selected information layers. Generally, in the case of an optical recording medium having multiple information layers, optical properties of information layers such as reflectivity, transmissivity and absorption rate, or thermal properties such as heat conduction, temperature elevation and cooling may vary continuously or discontinuously in laminated information layers. In the optical recording medium having such plurality of information layers, by learning in selected information layers out of all information layers, the optimum delay amount of other information layers can be predicted as continuously or discontinuously changing values from the learning results of the selected information layers.

[0114] Referring to a flowchart in Fig. 10, we will explain this recording method of optical recording medium. In this recording method of optical recording medium, the example is the optical recording medium 501 having three information layers 203a, 203b, 203c shown in Fig. 6. In this recording method, out of the three information layers, the information layer 203a of the remotest side from the light source and the information layer 203a at the closest side to the light source are selected for learning step. Of the following steps, steps 801 to 807 are substantially same as step 701 to 707 in the third embodiment.

(1) A laser beam is focused on the learning region (801).

(2) The leading edge delay amount change circuit 81 outputs a delay signal 49 discontinuously changing the leading edge delay amount depending on the combination of the recording mark length and the length of space preceding the mark. Then the leading edge pulse delay circuit 24 outputs a delayed leading edge pulse 50 according to the delay signal 49 (802).

(3) The trailing edge delay amount change circuit 82 outputs a delay signal 51 discontinuously changing the trailing edge delay amount depending on the combination of the recording mark length and the length of space succeeding

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the mark. Then the trailing edge pulse delay circuit 25 outputs a delayed trailing edge pulse 52 according to the delay signal 51 (803).

(4) The laser beam modulated by the recording pulses 28 according to the delayed leading edge pulse 50 and delayed trailing edge pulse 52 obtained at each step described above is emitted on the information layer 203c, and recording marks and spaces are formed (804).

(5) The formed recording marks and spaces are read, and a reproduction signal is obtained (805). From the reproduction signal, the leading edge interval between the adjacent recording mark leading edges and the trailing edge interval between the trailing edges can be measured.

(6) The leading edge interval and trailing edge interval is identified to be in a range of optimum length or not respectively (806). As a result, corresponding optimum leading edge delay amount and trailing edge delay amount are obtained in each combination of recording mark length and space preceding and succeeding the mark. The recording mark length, length of space preceding the mark, and combination of leading edge delay amount are leading edge position set values, and the recording mark length, length of space succeeding the mark, and combination of trailing edge delay amount are trailing edge position set values.

(7) Then, in each leading edge condition memory circuit of the leading edge condition selector 12, the above-mentioned leading edge position set value of the learning result relating to the information layer 203c is stored. Similarly, in each trailing edge condition memory circuit of the trailing edge condition selector 17, the above-mentioned trailing edge position set value is stored (807).

(8) It is identified if the procedure from step 802 to step 807 is finished in all selected information layers (808). In this example, the information layer 203a is not learned yet, and going back to step 802, learning of this information layer 203a is started. When learning is finished in all selected information layers, the process goes to next step.

(9) On the basis of the above-mentioned learning results of the selected information layers 203a, 203c out of all information layers, the optimum leading edge delay amount and trailing edge delay amount for other information layer 203b are calculated (809). At this calculating step 809, various methods of calculation are usable. In this example, by the interpolation method between the learning result from the information layer 203a closest to the light source and the learning result from the information layer 203c remotest from the light source, the leading edge delay amount and trailing edge delay amount of the intermediate information layer 203b are calculated. For example, in the combination of the same recording mark length and length of space preceding the mark, the intermediate value of the leading edge delay time obtained in the information layer 203a and the leading edge delay time obtained in the information layer 203c is calculated as the leading edge delay time of the information layer 203b. Herein, the information layers 203a, 203b, 203c are supposed to change continuously in the optical characteristics and thermal characteristics.

(10) In each one of the information layers 203a, 203b, 203c, the leading edge position set value and trailing edge starting position are recorded on the management region 208 of the optical recording medium 501 (810). At this step 802, the optimum leading edge delay amount and trailing edge delay amount can be determined by learning and interpolation in all information layers 203a, 203b, 203c, and can be respectively recorded as the leading edge position set value and trailing edge position set value.

[0115] Thus, by skipping the learning step in the information layer 203b, the time required for learning step can be shortened. If the optical recording medium is composed of four or more information layers, the set value of the delay amount of the recording pulse of the information layers at intermediate positions can be set by equally distributing the set values of each delay amount from the learning result from the information layer at the light source side and the learning result from the information layer remotest from the light source.

[0116] In the embodiment, as the method of interpolation, the learning results of two selected information layers are interpolated linearly, and the intermediate value is used as the optimum delay amount for the information layer at the intermediate position, but the method is not limited to this alone. For example, learning results of recording pulses in each information layer at intermediate position are measured preliminarily, and the interpolation coefficient (interpolation formula) may be calculated from the measured results of the characteristics. Afterwards, when executing the learning step in a multi-layer optical recording medium of similar type, it is possible to interpolate by using this interpolation coefficient (interpolation formula). In this case, by recording the obtained learning results and interpolation coefficient in and the like the recording apparatus of optical recording medium, the learning step can be executed in a multi-layer optical recording medium of similar type.

[0117] When composed of two information layers only learning steps are executed preliminarily in the information layer at light incident side and the information layer at the remote side. From the learning result of one information layer, the interpolation coefficient is determined about the optimum delay amount of other information layer. Afterwards, when executing the learning step in a similar optical recording medium, by executing the learning step in one information layer only, the optimum delay amount of other information layer can be interpolated. That is, it is enough to execute the learning procedure only in one information layer of the two information layers, the time required for learning pro-

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cedure can be shortened.

[0118] Although the present invention has been described in connection with the preferred embodiments thereof with reference to the accompanying drawings, it is to be noted that various changes and modifications are apparent to those skilled in the art. Such changes and modifications are to be understood as included within the scope of the present invention as defined by the appended claims, unless they depart therefrom.

## Claims

10. 1. An optical recording medium comprising:

a substrate;  
a protection plate; and

15 a plurality of information layers sandwiched between said substrate and said protection plate, for recording information signals as a length of a mark and a length of a space between two marks, the mark being formed when irradiated by light received through said substrate,

wherein said plurality of information layers have a management region recording a leading edge recording condition and a trailing edge recording condition,

20 wherein the leading edge recording condition is used to change a recording start position for forming a leading edge of the mark, the leading edge recording condition is depending on which information layer is recorded,  
wherein the trailing edge recording condition is used to change a recording end position for forming a trailing edge of the mark, the trailing edge recording condition is depending on which information layer is recorded.

25 2. An optical recording medium according to claim 1, wherein said leading edge recording condition includes a condition depending on the length of said mark and the length of the space preceding said mark, and said trailing edge recording condition includes a condition depending on the length of said mark and the length of the space succeeding said mark.

30 3. An optical recording medium according to claim 1, wherein said leading edge recording condition and said trailing edge recording condition includes a condition of whether data have been recorded or not in one of said information layers positioned at light incident side.

4. An optical recording medium according to any one of claims 1 to 3, wherein said management region is provided in one of said plurality of information layers.

35 5. An optical recording medium according to claim 4, wherein said management region is provided in one of said information layers at light incident side.

6. An optical recording medium according to claim 1, wherein each of said plurality of information layers has a identifier for information layer.

45 7. An optical recording medium according to claim 1, wherein each of said plurality of information layers has a recording identifier on whether data have been recorded or not therein.

8. An optical recording medium according to claim 7, wherein said recording identifier is recorded in said management region.

50 9. An optical recording medium comprising:

a substrate;  
a protection plate; and

55 a plurality of information layers sandwiched between said substrate and said protection plate, for recording information signals as a length of a mark and a length of a space between two marks, the mark being formed when irradiated by light received through said substrate,

wherein said plurality of information layers have a learning region provided for leading edge position learning for determining an optimum value of recording start position used for forming the leading edge of said mark, and

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trailing edge position learning for determining an optimum value of for the recording end position for forming the trailing edge of the mark to be recorded.

5 10. An optical recording medium according to claim 9, wherein each of said plurality of information layers has the learning region.

11. An optical recording medium according to claim 9, wherein the optimum value of said recording start position and that of said recording end position include information on information layer to be recorded, the length of mark, and the length of the space preceding and succeeding the marks.

10 12. An optical recording medium according to claim 11, wherein said plurality of information layers have a recording region for recording the optimum values of said recording start position and recording end position obtained by the learning.

15 13. An optical recording medium according to claim 9, the plurality of information layers further comprising another learning region for learning said leading edge position and said trailing edge position when the information layer at light incident side of the information layer to be recorded includes recorded data and when it does not include recorded data.

20 14. A recording method of optical recording medium for recording information signals in an information layer as a length of mark and a length of a space between two marks, the mark being formed when irradiated by light received, the recording method comprising:

25 setting a leading edge starting condition used to change a recording start position for forming a leading edge of the mark depending on the length of the mark, the length of the space preceding said mark, and the information layer to be recorded;

setting a trailing edge starting condition used to change a recording end position for forming a trailing edge of the mark depending on the length of said mark, the length of the space succeeding the mark, and the information layer to be recorded; and

30 recording information signals by forming marks when irradiated by light received through the information layer after setting the leading edge starting condition and setting the trailing edge starting condition.

15 16. A recording method of optical recording medium according to claim 14, wherein said recording start and end positions are changed for recording depending on a recorded or unrecorded state of the upper information layer positioned at light incident side.

35 17. A recording method of optical recording medium according to claim 16, wherein if recorded state and unrecorded state coexist in said upper information layer, the recording start position is set as a position between the recording start positions in unrecorded state and in recorded state, and the recording end position is set as a position between the recording end positions in unrecorded state and in recorded state.

40 18. A recording method of optical recording medium according to claim 15, wherein said recording start position and said recording end position are corrected based on quantity of light reflected from the upper information layer positioned at light incident side of the information layer to be recorded.

45 19. A recording method of optical recording medium having a plurality of information layers for recording information signals as a length of a mark and a length of a space between two marks, the mark being formed when irradiated by light received, the recording method comprising, prior to recording said information:

50 learning a leading edge to determine an optimum value of recording start position for forming the leading edge of the mark by trial writing of the mark by changing the information layer to be recorded, the length of the mark, and the length of the space preceding the mark, and

55 learning a trailing edge to determine an optimum value of recording end position for forming the trailing edge of said mark by trial writing of the mark by changing the information layer to be recorded, the length of the mark, and the length of the space succeeding the mark.

19. A recording method of optical recording medium according to claim 18, further comprising:

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selecting at least two information layers of said plurality of information layers; executing said leading edge learning and said trailing edge learning therefor; and calculating optimum values of the recording start position and the recording end position of other information layer than said selected information layers, on the basis of the learning results at said leading edge learning and said trailing edge learning.

5 20. A recording method of optical recording medium according to claim 18, wherein the optimum values of said recording start position and said recording end position obtained as a result of learning at said leading edge learning step and said trailing edge learning step are recorded in said plurality of information layers.

10 21. A recording method of optical recording medium according to claim 18, wherein the optimum values of said recording start position and said recording end position obtained as a result of learning at said leading edge learning step and said trailing edge learning step are recorded in a storage device provided in a recording apparatus for the optical recording medium.

15 22. A recording method of optical recording medium according to claim 18, wherein an information about said leading edge recording condition and a information about said trailing edge recording condition recorded in said management region are used as initial values at said leading edge learning and at said trailing edge learning.

20 23. A recording method of optical recording medium according to claim 18, wherein said leading edge learning and said trailing edge learning are executed when detecting a change in recorded state of the upper information layer positioned at light incident side of said information layer to be recorded.

25 24. A recording method of optical recording medium according to claim 18, wherein said leading edge learning and said trailing edge learning are executed when detecting a change in a quantity of light reflected from the information layer to be recorded.

30 25. A recording apparatus of optical recording medium having a plurality of information layers for recording information signals as a length of a mark and a length of a space between two marks, the mark being formed when irradiated by light received, the recording apparatus comprising:

a layer detector circuit detecting which one of said plurality of information layers is an information layer to be recorded;

35 a basic pulse generator generating a pulse of a predetermined width at a leading edge position of a mark to be formed, generating pulse at the middle of said mark, and generating a pulse of a predetermined width at a trailing edge position of said mark;

a data length detector composed of a recording mark detector circuit detecting a length of the mark, a preceding space detector circuit detecting the length of a space preceding said mark, a succeeding mark detector circuit detecting the length of a mark succeeding said mark, and a succeeding space detector circuit detecting the length of a space succeeding said mark;

40 a timing controller composed of a leading edge pulse delay circuit generating a delayed leading edge pulse by setting a delay amount of the leading edge pulse from an output signals of said recording mark detector circuit, said preceding space detector circuit and said layer detector circuit, and a trailing edge pulse delay circuit generating a delayed trailing edge pulse by setting a delay amount of the trailing edge pulse from an output signals of said recording mark detector circuit, said succeeding space detector circuit and said layer detector circuit;

45 a pulse synthesizer forming a recording pulse by synthesizing said delayed leading edge pulse signal, said intermediate pulse signal, and said delayed trailing edge pulse signal; and a laser driver modulating an intensity of a beam on the basis of the recording pulse.

50 26. A recording apparatus of optical recording medium according to claim 25, further comprising a timing controller comprising:

a recording identifying circuit detecting a recorded state of the upper information layer positioned at light incident side;

55 a leading edge pulse delay circuit generating a delayed leading edge pulse by setting a delay amount of said leading edge pulse from an output signal of said recording mark detector circuit, said preceding space detector circuit, said layer detector circuit, and said recording identifying circuit, and

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a trailing edge pulse delay circuit generating a delayed trailing edge pulse by setting a delay amount of said trailing edge pulse from an output signal of said recording mark detector circuit, said succeeding space detector circuit, said layer detector circuit, and said recording identifying circuit.

- 5 27. A recording apparatus of optical recording medium according to claim 25, further comprising selector selecting the delay amount of leading edge pulse and delay amount of trailing edge pulse, from the leading edge starting information and trailing edge starting information recorded in said management region, according to the identifying result of said layer detector circuit.
- 10 28. A recording apparatus of optical recording medium according to claim 26, further comprising selector selecting the delay amount of leading edge pulse and delay amount of trailing edge pulse, from the leading edge starting information and trailing edge starting information recorded in said management region, according to the identifying result of said recording identifying circuit.
- 15 29. A recording apparatus of optical recording medium according to claim 25, further comprising a detector detecting a quantity of light reflected from the information layer to be recorded,  
wherein the delay amount of leading edge pulse and delay amount of trailing edge pulse are determined based on the detecting result obtained by said reflected light quantity detecting means.
- 20 30. A recording apparatus of optical recording medium according to claim 27,  
wherein said layer detector circuit detects the layer identifier for identifying the information layer.
- 25 31. A recording apparatus of optical recording medium according to claim 28, wherein said recording identifying circuit detects the recording identifier for distinguishing whether the information layer is in recorded state or in unrecorded state.
- 30 32. A recording apparatus of optical recording medium having a plurality of information layers for recording information signals as a length of a mark and a length of a space between two marks, the mark being formed when irradiated by light received, the recording apparatus comprising:
  - 35 a layer detector detecting which one of the plurality of information layers is a information layer to be recorded; a leading edge learning means for determining an optimum value of delay amount of leading edge pulse depending on a position of the information layer to be recorded, a length of a mark, and a length of a space preceding the mark; and  
a trailing edge learning means for determining an optimum value of delay amount of trailing edge pulse depending on a position of a information layer to be recorded, a length of a mark, and a length of a space succeeding a recording mark.
  - 40 33. A recording apparatus of optical recording medium according to claim 32, further comprising operating means for operating the leading edge learning means and trailing edge learning means by selecting at least two information layers out of said plurality of information layers, and calculating the optimum value of recording start position and the optimum value of recording end position of other information layers from the operating results of said leading edge learning means and said trailing edge learning means.
  - 45 34. A recording apparatus of optical recording medium according to claim 32, further comprising learning result recording device recording the learning result information obtained from said leading edge learning means and said trailing edge learning means on the optical recording medium.
  - 50 35. A recording apparatus of optical recording medium according to claim 33, wherein said leading edge learning means and said trailing edge learning means use the leading edge recording information and trailing edge recording information recorded in said management region as initial values.
  - 55 36. A recording apparatus of optical recording medium according to claim 33, wherein said leading edge learning means and said trailing edge learning means further calculate when detecting a change in the recorded or unrecorded state of each information layer.
  - 60 37. A recording apparatus of optical recording medium according to claim 33, wherein said leading edge learning means and said trailing edge learning means further calculate when detecting a change in the quantity of light

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reflected from the information layer to be recorded.

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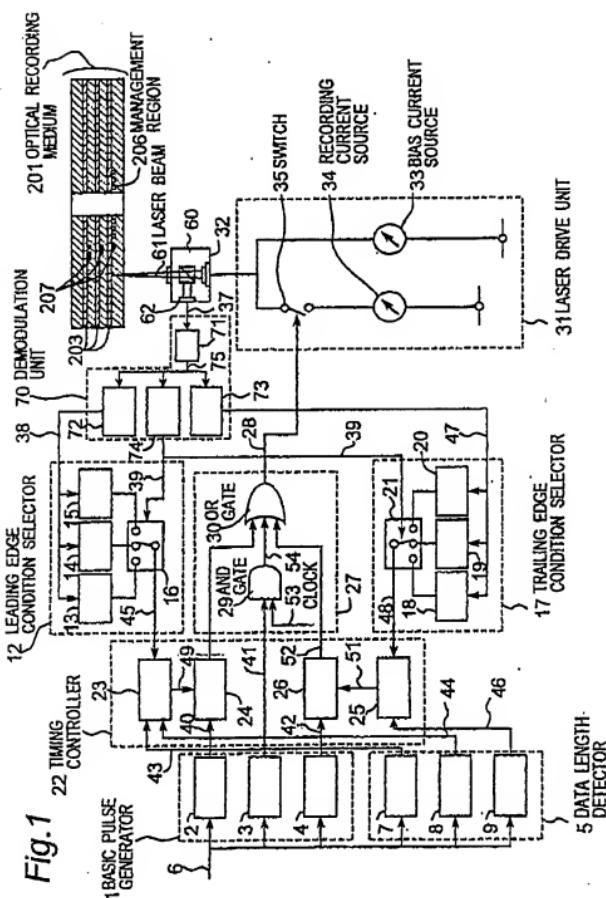
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Fig. 1



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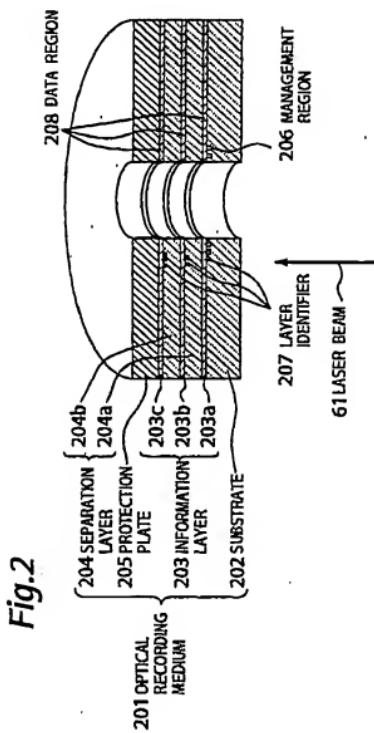
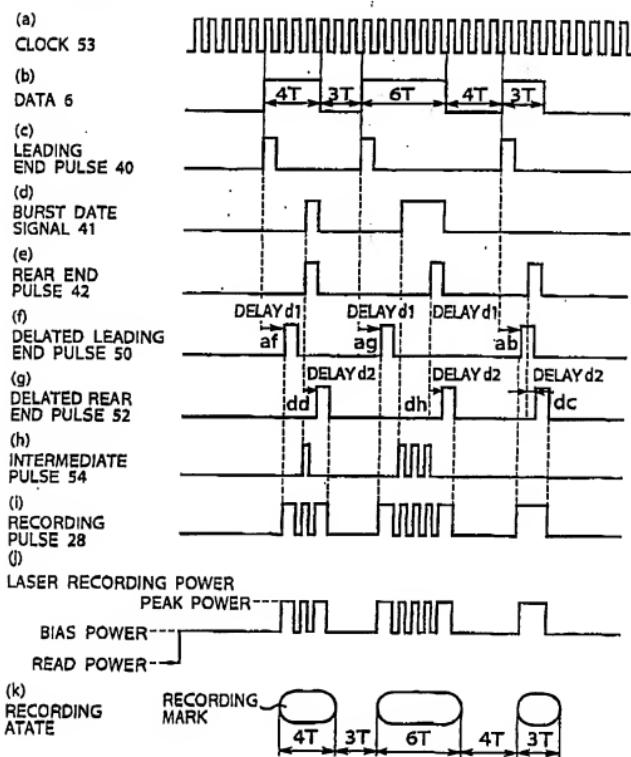


Fig.2

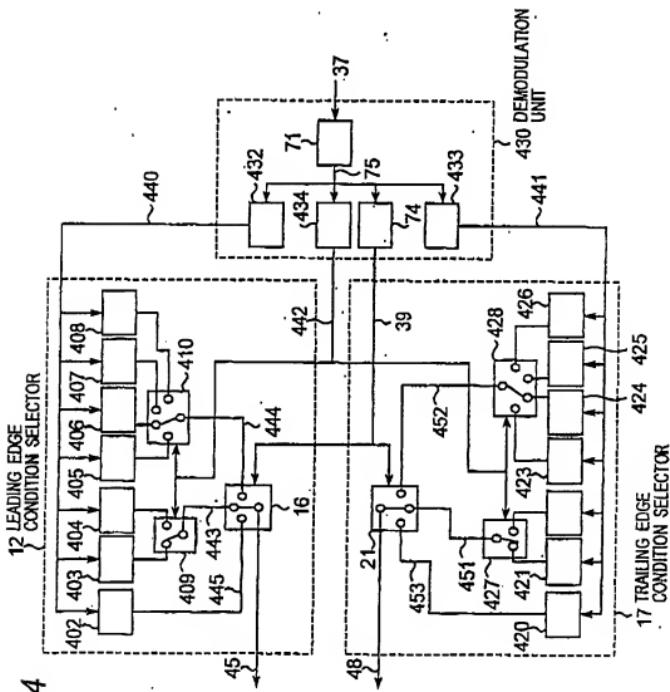
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Fig.3



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Fig. 4



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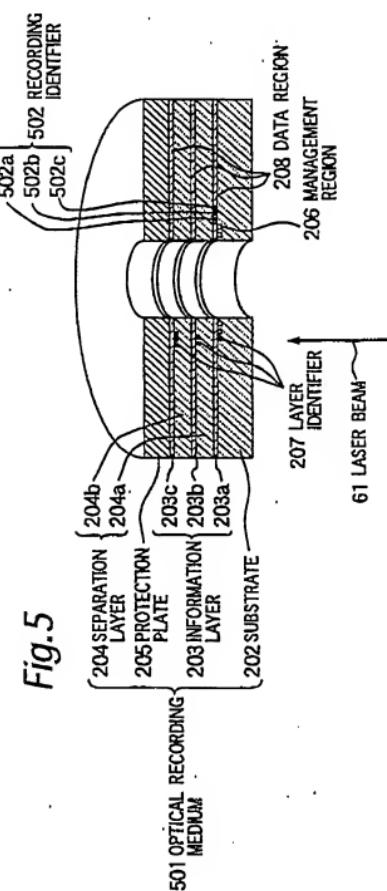


Fig. 5

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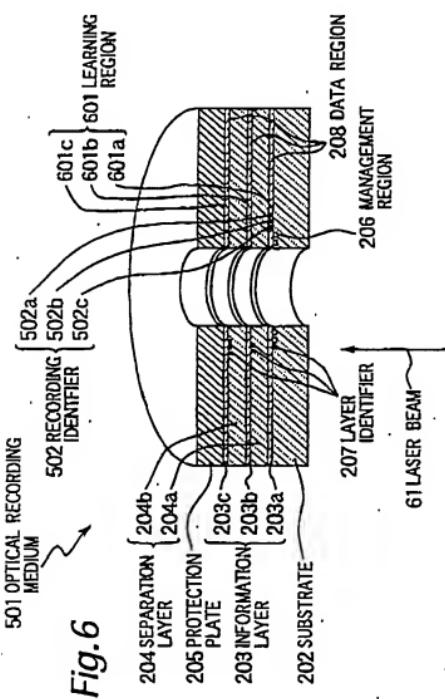
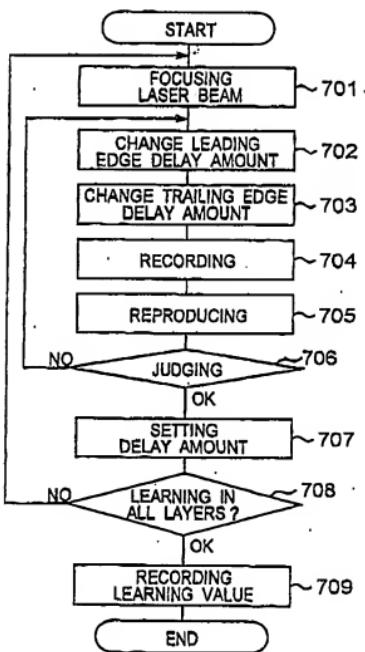


Fig. 6

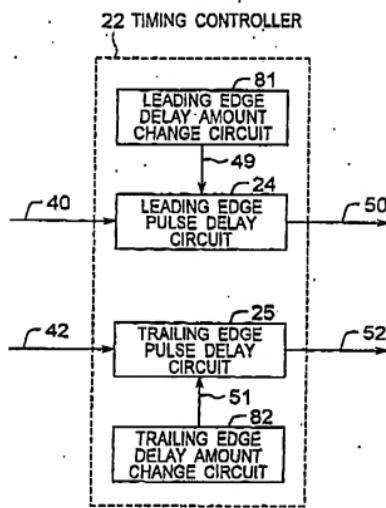
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Fig.7



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Fig.8



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Fig.9B

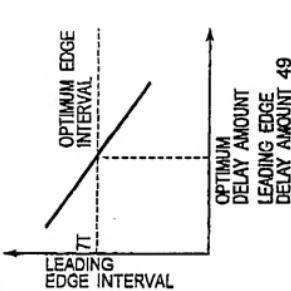


Fig.9A

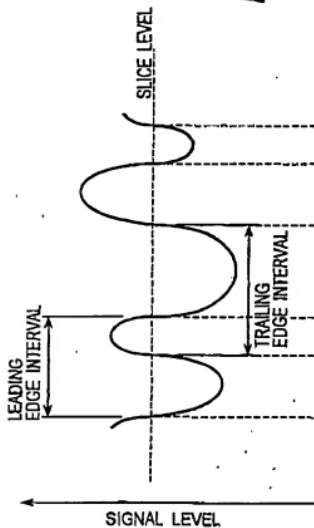
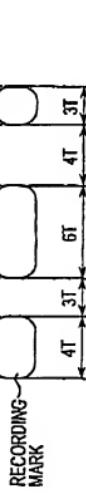
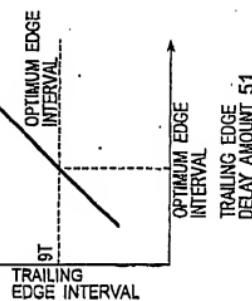
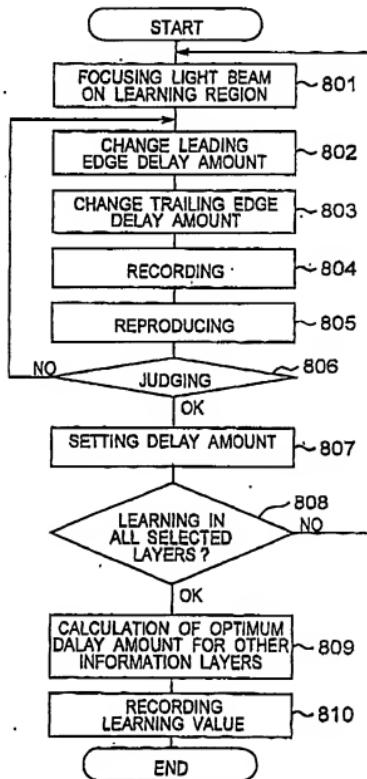


Fig.9C



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Fig.10



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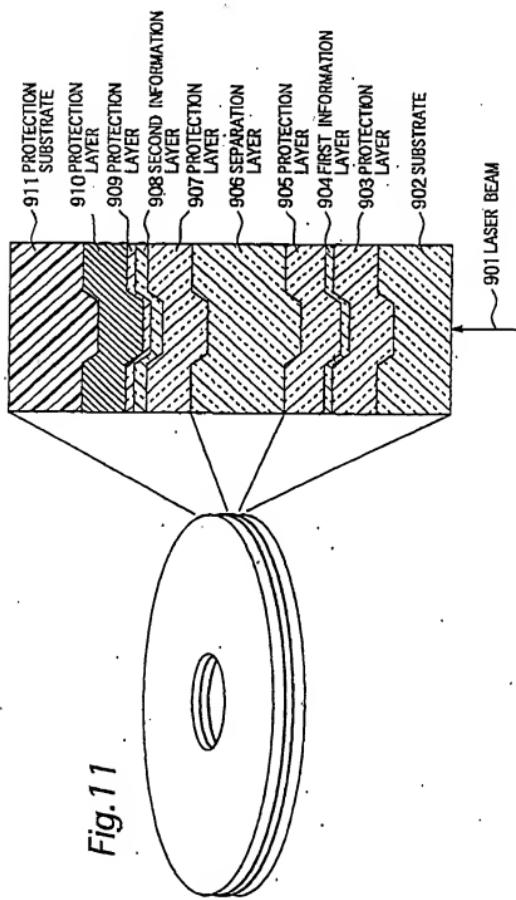


Fig. 11

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| INTERNATIONAL SEARCH REPORT                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                                                                                                                                | International application No.<br>PCT/JP01/08690                                   |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------|
| <b>A. CLASSIFICATION OF SUBJECT MATTER</b><br>Int. Cl? G11B7/007, G11B7/0045, G11B7/24                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                |                                                                                   |
| According to International Patent Classification (IPC) or to both national classifications and IPC                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                                                                                                                                |                                                                                   |
| <b>B. FIELDS SEARCHED</b><br>Minimum documentation searched (classification system followed by classification symbols)<br>Int. Cl? G11B7/00-7/013, G11B7/24                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                                                                                                                                |                                                                                   |
| Documentation searched other than minimum documentation to the extent that such documents are included in the file(s) searched<br>Jitsuyou Shinan Kobo 1923-1996 Tokoro Jitsuyou Shinan Kobo 1994-2001<br>Kokai Jitsuyou Shinan Kobo 1971-2001 Jitsuyou Shinan Tokoro Kobo 1998-2001                                                                                                                                                                                                                                                                                                                                                                       |                                                                                                                                |                                                                                   |
| Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |                                                                                                                                |                                                                                   |
| <b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                                                                                                                                |                                                                                   |
| Category <sup>a</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | Classification of document, with indication, where appropriate, of the relevant passages                                       | Relevant to claim No.                                                             |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | JP 11-38520 A (Nikon Corporation),<br>06 January, 1999 (06.01.99),<br>Full text (Family: none)                                 | 1-3?                                                                              |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | WO 97/14142 A1 (Matsushita Electric Ind. Co., Ltd.),<br>17 April, 1997 (17.04.97),<br>Full text<br>& US 5745467 A EP 797193 A1 | 1-3?                                                                              |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | EP 517490 A2 (IBM),<br>08 December, 1992 (09.12.92),<br>Full text<br>& JP 5-151644 A & US 5255262 A                            | 4-6,30                                                                            |
| P,Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | JP 2000-293967 A (Matsushita Electric Ind. Co., Ltd.),<br>20 October, 2000 (20.10.00),<br>Full text (Family: none)             | 7,8,15-17,31                                                                      |
| <input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> Same patent family name.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                                                                                                                                |                                                                                   |
| * Special categories of cited documents:<br>"A" document containing the general state of the art which is not considered to be relevant to the application but cited to indicate the level of knowledge in the field.<br>"B" earlier document but published on or after the international filing date<br>"C" document which may throw doubt on priority claimed or which is cited to establish the publication date of another citation or other special reason (as specified)<br>"D" document referring to a oral disclosure, use, exhibition or other document published prior to the international filing date but later than the priority date claimed |                                                                                                                                |                                                                                   |
| Date of the actual compilation of the international search<br>16 December, 2001 (16.12.01)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |                                                                                                                                | Date of mailing of the international search report<br>15 January, 2002 (15.01.02) |
| Name and mailing address of the ISA/<br>Japanese Patent Office                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |                                                                                                                                | Authorized officer                                                                |
| Facsimile No.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                                                                                                                                | Telephone No.                                                                     |

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International application No.  
PCT/JP01/08690

| C(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT |                                                                                                                                                          |                       |
|-----------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| Category*                                           | Character of document, with indication, where appropriate, of the relevant passages                                                                      | Relevant to claim No. |
| V                                                   | EP 984461 A1 (Matsushita Electric Ind. Co., Ltd.),<br>08 March, 2000 (08.03.00),<br>Full text<br>& US 6188656 B1 4 JP 2000-2321719 A<br>& WO 00/15322 A1 | 1-37                  |
| A                                                   | US 5756265 A (Matsushita Electric Ind. Co., Ltd.),<br>26 May, 1998 (26.05.98),<br>Full text<br>& JP 9-54589 A                                            | 1-37                  |

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